

WEST Search History

DATE: Friday, September 24, 2004

Hide? Set Name Query**Hit Count***DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR*

<input type="checkbox"/>	L34	L33 and (peripheral same specification)	1
<input type="checkbox"/>	L33	L32 and file	3
<input type="checkbox"/>	L32	L30 and (memory same register)	3
<input type="checkbox"/>	L31	L30 and ((array or (data same structure)) same memory)	2
<input type="checkbox"/>	L30	L29 and (peripheral same register)	3
<input type="checkbox"/>	L29	L28 and (functional same test\$ same register)	3
<input type="checkbox"/>	L28	L27 and bit and location	26
<input type="checkbox"/>	L27	L26 and (register same state)	26
<input type="checkbox"/>	L26	L25 and (functional same (attribute or parameter or variable))	35
<input type="checkbox"/>	L25	L24 and (peripheral same (hdl or register or rtl))	79
<input type="checkbox"/>	L24	l23 and (peripheral same interfac\$)	79
<input type="checkbox"/>	L23	L21 and (register same (based or base or definition or independent))	85
<input type="checkbox"/>	L22	L21 and (register same object)	48
<input type="checkbox"/>	L21	L20 and (function\$ same file)	87
<input type="checkbox"/>	L20	L19 and simulation and (hdl or language or vhdl) and (test or testbench)	96
<input type="checkbox"/>	L19	l17 and L18	1311
<input type="checkbox"/>	L18	register same file	22362
<input type="checkbox"/>	L17	peripheral same register	13272
<input type="checkbox"/>	L16	l13 not L15	8
<input type="checkbox"/>	L15	L14 and (register same state)	40
<input type="checkbox"/>	L14	L13 and (register same (hdl or vhdl or structure))	40
<input type="checkbox"/>	L13	L3 and (register same definition)	48
<input type="checkbox"/>	L12	L11 and (parameter or attribute)	47
<input type="checkbox"/>	L11	L3 and (register same base\$4 same test\$)	47
<input type="checkbox"/>	L10	L1 and backus	2
<input type="checkbox"/>	L9	L3 and backus	1
<input type="checkbox"/>	L8	L7 and (register same parameter)	39
<input type="checkbox"/>	L7	L6 and attribute	61
<input type="checkbox"/>	L6	L3 and (data same structure)	113
<input type="checkbox"/>	L5	L4 and attribute	31
<input type="checkbox"/>	L4	L3 and (register same state)	83
<input type="checkbox"/>	L3	L2 and compil\$	140
<input type="checkbox"/>	L2	L1 and (simulat\$ same functional)	219

☐ L1 silicon and simulation and register and peripheral

1092

END OF SEARCH HISTORY